

ABSTRACT OF THE DISCLOSURE

A driving circuit that drives a capacitive load to a target voltage within a power supply voltage range, includes: a first amplifier circuit having a first operating range, for charging and driving an output terminal and a second amplifier circuit having a second operating range, for discharging and driving the output terminal, and an input control circuit for supplying one of a voltage at an upper limit side (V1) of a range common to the first and second operating ranges, a voltage at a lower limit side (V2) of the range, and a target voltage (Vin) to an input terminal of the first or second amplifier circuit are provided. A driving period for driving the output terminal to the target voltage includes a first period (T1) during which the input control circuit supplies the voltage (V1) or the voltage (V2) to the input terminals of the first and second amplifier circuits and a second period (T2) for supplying the target voltage (Vin) to the input terminals of the first and second amplifier circuits.